

# Some Comments on Morel and Renvoise's 'Global Optimization by Suppression of Partial Redundancies'

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**Abstract:** There has been much continuing interest in the global optimization method of Morel and Renvoise as extended by Joshi-Dhamdere and Chow. Unfortunately, the original formulation of Morel and Renvoise and those of Joshi-Dhamdere and Chow do not agree as to the form of the algorithm. The precise form of the algorithm's boolean equations is critical to the algorithm's performance and efficient implementation. Some of the problems with the algorithm noted by other authors can be solved by using a slightly different formulation of the algorithm.

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## I. Morel and Renvoise's Algorithm

There has been much continuing interest in the global optimization method of Morel and Renvoise [1] as extended by Joshi-Dhamdere [2] and Chow [3]. For example, papers by Drechler and Stadel [4], Sorkin [5], Dhamdere [6], and Knoop, Rütting and Steffan [7] deal with perceived problems with the original algorithm. Unfortunately, the original formulation of Morel and Renvoise and those of Joshi-Dhamdere [2] and Chow [3] do not agree as to the form of the algorithm. The precise form of the algorithm's boolean equations is critical to the algorithm's performance and efficient implementation. Some of the problems with the algorithm noted by other authors can be solved by using a slightly different formulation of the algorithm.

Morel and Renvoise presented the following system of Boolean equations:

### AVAILABILITY

$$\left\{ \begin{array}{l} \text{AVIN}_b = \prod_{p \in \text{Pred}(b)} \text{AVOUT}_p \\ \text{AVOUT}_b = \text{COMP}_b + \text{AVIN}_b * \text{TRANSP}_b \end{array} \right\}$$

### *ANTICIPABILITY*

$$\left\{ \begin{array}{l} \text{ANTOUT}_b = \prod_{s \in \text{Succ}(b)} \text{ANTIN}_s \\ \text{ANTIN}_b = \text{ANTLOC}_b + \text{ANTOUT}_b * \text{TRANSP}_b \end{array} \right\}$$

### *PARTIAL AVAILABILITY*

$$\left\{ \begin{array}{l} \text{PAVIN}_b = \sum_{p \in \text{Pred}(b)} \text{PAVOUT}_p \\ \text{PAVOUT}_b = \text{COMP}_b + \text{PAVIN}_b * \text{TRANSP}_b \end{array} \right\}$$

### *POTENTIAL PLACEMENT*

$$\left\{ \begin{array}{l} \text{PPIN}_b = \text{CONST}_b * (\text{ANTLOC}_b + \text{PPOUT}_b * \text{TRANSP}_b) * \prod_{p \in \text{Pred}(b)} (\text{PPOUT}_p + \text{AVOUT}_p) \\ \text{CONST}_b = \text{ANTIN}_b * (\text{PAVIN}_b + \text{TRANSP}_b * \sim \text{ANTLOC}_b) \\ \text{PPOUT}_b = \prod_{s \in \text{Succ}(b)} \text{PPIN}_s \end{array} \right\}$$

### *INSERTION*

$$\text{INSERT}_b = \text{XPPOUT}_b * (\sim (\text{AVOUT}_b + \text{XPPIN}_b * \text{TRANSP}_b))$$

### *DELETION*

$$\text{DELETE}_b = \text{ANTLOC}_b * \text{XPPIN}_b$$

## **II. Joshi and Dhamdere's Equations**

Joshi and Dhamdere noted that the PPIN/PPOUT computation duplicated the ANTIN/ANTOUT computation, making the *ANTICIPABILITY* system of equations unnecessary. Their formulation of *POTENTIAL PLACEMENT* also eliminates the CONST factor, so the *PARTIAL AVAILABILITY* system of equations is also unused.

### *POTENTIAL PLACEMENT (Joshi-Dhamdere)*

$$\left\{ \begin{array}{l} \text{PPIN}_b = (\text{ANTLOC}_b + \text{PPOUT}_b * \text{TRANSP}_b) * \prod_{p \in \text{Pred}(b)} (\text{PPOUT}_p + \text{AVOUT}_p) \\ \text{PPOUT}_b = \prod_{s \in \text{Succ}(b)} \text{PPIN}_s \end{array} \right\}$$

### III. Chow's Equations

In Chow's formulation, the *ANTICIPABILITY* system of equations is present, but the *CONST* factor is simplified in the *POTENTIAL PLACEMENT* system.

*POTENTIAL PLACEMENT (Chow)*

$$\left\{ \begin{array}{l} \text{PPIN}_b = \text{CONST}_b * (\text{ANTLOC}_b + \text{PPOUT}_b * \text{TRANSP}_b) * \prod_{p \in \text{Pred}(b)} (\text{PPOUT}_p + \text{AVOUT}_p) \\ \text{CONST}_b = \text{ANTIN}_b * \text{PAVIN}_b \\ \text{PPOUT}_b = \prod_{s \in \text{Succ}(b)} \text{PPIN}_s \end{array} \right\}$$

In Chow's analysis of the algorithm, it was noted that the *PAVIN* factor in *CONST* is critical to limiting the distance that partial redundancies are hoisted. This is necessary in order to limit the lifetimes of hoisted expressions, which minimizes register contention. The lack of this factor in the Joshi and Dhamdere formulation actually maximizes the distance that hoisted expressions are moved, which greatly increases register contention.

### IV. Combined Equations

Joshi and Dhamdere were correct in eliminating the *ANTICIPABILITY* system of equations, and Chow was correct in retaining the *PAVIN* factor, so if we combine all three formulations of the algorithm, we get the following system of equations:

*AVAILABILITY*

$$\left\{ \begin{array}{l} \text{AVIN}_b = \prod_{p \in \text{Pred}(b)} \text{AVOUT}_p \\ \text{AVOUT}_b = \text{COMP}_b + \text{AVIN}_b * \text{TRANSP}_b \end{array} \right\}$$

*PARTIAL AVAILABILITY*

$$\left\{ \begin{array}{l} \text{PAVIN}_b = \sum_{p \in \text{Pred}(b)} \text{PAVOUT}_p \\ \text{PAVOUT}_b = \text{COMP}_b + \text{PAVIN}_b * \text{TRANSP}_b \end{array} \right\}$$

*POTENTIAL PLACEMENT*

$$\left\{ \begin{array}{l} \text{PPIN}_b = \text{PAVIN}_b * (\text{ANTLOC}_b + \text{PPOUT}_b * \text{TRANSP}_b) * \prod_{p \in \text{Pred}(b)} (\text{PPOUT}_p + \text{AVOUT}_p) \\ \text{PPOUT}_b = \prod_{s \in \text{Succ}(b)} \text{PPIN}_s \end{array} \right\}$$

This formulation of the algorithm has a number of advantages. First, it limits hoisting distance and, therefore, expression lifetimes and register contention. This is critical on real hardware where the number of registers available is limited. It also eliminates the unnecessary *ANTICIPABILITY* system of equations, so there is one less system of equations to compute. An analysis of the lifetimes of the terms of the above equations shows that only 7 booleans are in use per basic block at any one time, so the amount of space required to compute the equations is to  $7 * BB * EXP$  bits, where *BB* is the number of basic blocks and *EXP* is the number of expressions.

The slow convergence of the bi-directional *POTENTIAL PLACEMENT* system of equations has been one of the major problems with the algorithm addressed by other authors. Perhaps the most interesting advantage of this formulation is that the *PPIN* equation is dominated by the *PAVIN* factor. This means that the *POTENTIAL PLACEMENT* system of equations can be treated as if it were a uni-directional system instead of a bi-directional system and, at least for “normal” flow graphs [5], will converge rapidly when iterated in bottom-up, depth-first order. It is not necessary, and, is, in fact, incorrect and inefficient to iterate bi-directionally.

The intuitive reason for this result is that for each expression, the *PAVIN* factor initializes to true precisely those bits for basic blocks where the expression *might* be moved. In the Joshi-Dhamdere formulation, for example, this information has to propagate from the basic blocks where *ANTLOC* is true via iteration of the booleans.

This formulation of the Morel and Renvoise algorithm can also be used to do strength-reduction by extending the definitions of *COMP*, *ANTLOC* and *TRANSP* in accordance with Chow [2]. It can also be extended to implement induction expression elimination.

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